

TECHNICAL DATA DATA SHEET D0324 REV.-

# SILICON CARBIDE 1200V 41A POWER MOSFET DIE

**Applications:** 

- Solar inverters Switch Mode Power Supplies High voltage DC/DC converters
- Battery charges Mode drive Pulsed power application

### Features:

- High blocking voltage with low on-resistance
- High Speed Switching with low capacitances
- Easy to parallel and simple to drive
- Avalanche ruggedness
- Resistant to latch-up
- Silver back metal

#### **Maximum Ratings**@T<sub>A</sub>=25°C unless otherwise specified:

Characteristics	Symbol	Condition	Max.	Units
Drain - Source Voltage	$V_{DSmax}$	$V_{GS}$ = 0 V, I <sub>D</sub> = 100 µA	1200	V
Gate - Source Voltage (dynamic)	V <sub>GSmax</sub>	AC (f >1 Hz)	-10/+25	V
Gate - Source Voltage (static)	$V_{GSop}$	Static	-5/+20	V
Continuous Drain Current	ID	$V_{GS}$ =20 V, $T_{C}$ = 25°C	41	А
Pulsed Drain Current	I <sub>D(pulse)</sub>	Pulse width $t_P$ limited by $T_{jmax}$	82	А
Operating Junction and Storage Temperature	T」, T <sub>stg</sub>		-55 to +175	°C
Maximum Processing Temperature	T <sub>Proc</sub>	10 min. maximum	325	°C

(1) When using MOSFET body diode  $V_{GSmax} = -5V/+25V$ 

(2) MOSFET can also safely operate at  $V_{GS}$  = 0/+20 V

(3) Assumes a  $R_{\theta JC} < 0.35$  K/W

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#### TECHNICAL DATA DATA SHEET D0324 REV.-Electrical Characteristics@T<sub>4</sub>=25°C unless of

**Electrical Characteristics**@ $T_A$ =25°C unless otherwise specified:

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Units
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 V, I_{D} = 1 mA$	1200			V
Gate Threshold Voltage		$V_{DS} = V_{GS}$ , $I_D = 10mA$	2.0	2.8	4	V
	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 10mA, T_J = 175 \ ^{\circ}C$		1.8		•
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V		0.1	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V			100	nA
Drain-Source On-State Resistance		V <sub>GS</sub> = 20 V, I <sub>D</sub> = 20A		77	100	mΩ
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C		137		•
Trans conductance		V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 40 A		10.5		S
	<b>g</b> fs	V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 40 A, T <sub>J</sub> = 175 °C		8		•
Input Capacitance	Ciss	V <sub>GS</sub> = 0 V		1324		pF
Output Capacitance	Coss	V <sub>DS</sub> =1000V		74		
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1 MHz VAC = 25 mV		3.4		
Coss Stored Energy	E <sub>oss</sub>			37		μJ
Internal Gate Resistance	R <sub>G(int)</sub>	f = 1 MHz, $V_{AC}$ = 25 mV, ESR of C <sub>ISS</sub>		3.3		Ω
Gate to Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -5/20 V		23		nC
Gate to Drain Charge	Q <sub>gd</sub>	$I_{\rm D} = 50 \rm A$		14		
Total Gate Charge	Qg	Per IEC60747-8-4 pg 83		54		

## **Reverse Diode Characteristics:**

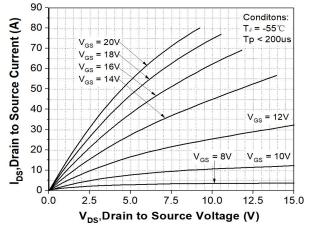
Characteristics	Symbol	Condition	Тур.	Max.	Units
		$V_{GS}$ = - 5 V, I <sub>SD</sub> = 10 A	4.0		V
Diode Forward Voltage	$V_{SD}$	$V_{GS}$ = - 5 V, $I_{SD}$ = 10 A, $T_{J}$ = 175 °C	3.5		V
Continuous Diode Forward Current	ls	T <sub>C</sub> = 25 °C		41	
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS}$ = - 5 V, $I_{SD}$ = 40 A , $T_{J}$ = 25 °C	25		ns
Reverse Recovery Charge	Qrr	V <sub>R</sub> = 800 V	102		nC
Peak Reverse Recovery Current	I <sub>rrm</sub>	dif/dt = 1950 A/µs	6.7		А

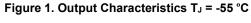
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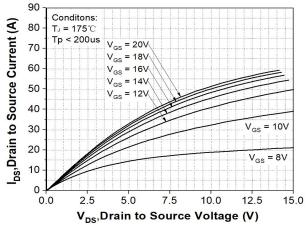
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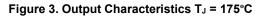


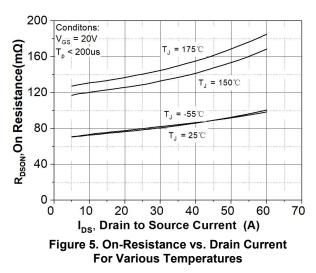
## TECHNICAL DATA DATA SHEET D0324 REV.-Typical Performance:











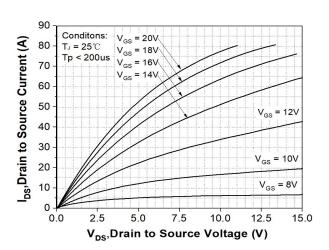


Figure 2. Output Characteristics T<sub>J</sub> = 25 °C

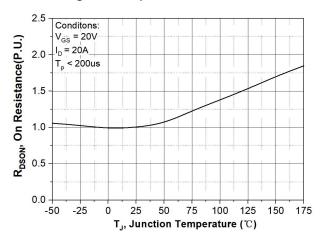
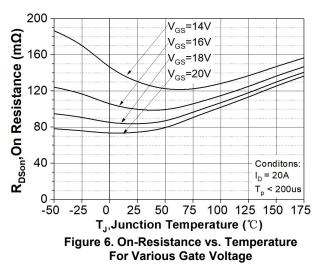


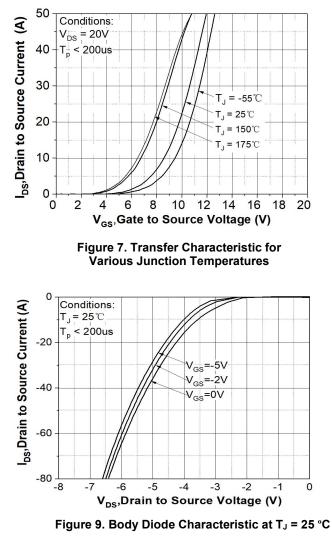
Figure 4. Normalized On-Resistance vs. Temperature



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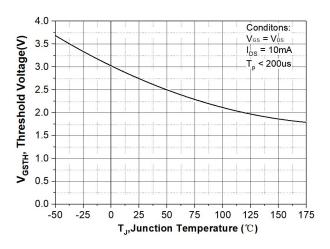


Figure 11. Threshold Voltage vs. Temperature

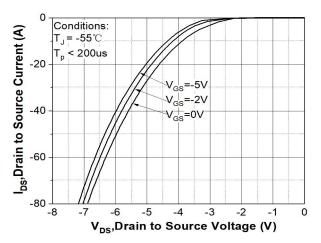


Figure 8. Body Diode Characteristic at T<sub>J</sub> = -55 °C

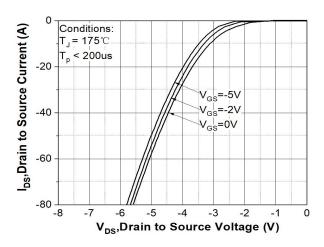
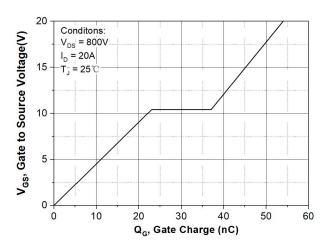


Figure 10. Body Diode Characteristic at T<sub>J</sub> = 175 °C





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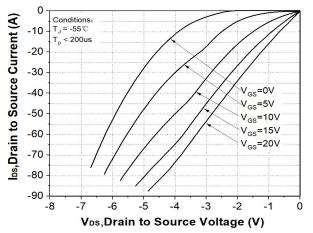


Figure 13. 3rd Quadrant Characteristic at T<sub>J</sub> = -55 °C

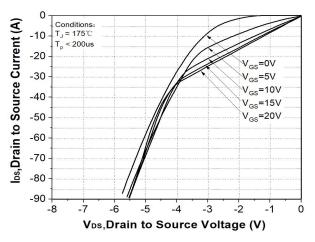
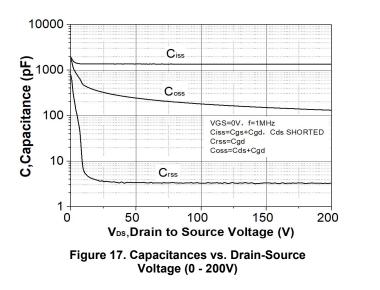


Figure 15. 3rd Quadrant Characteristic at T<sub>J</sub> = 175°C



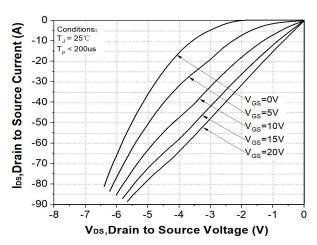


Figure 14. 3rd Quadrant Characteristic at T<sub>J</sub> = 25 °C

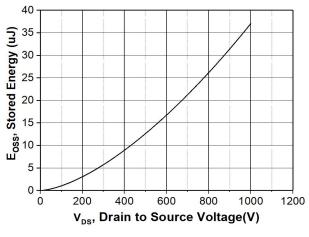
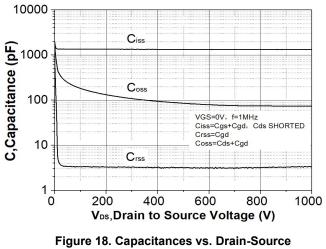


Figure 16. Output Capacitor Stored Energy



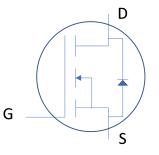
Voltage (0 - 1000V)

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## **Mechanical Dimensions**



Parameter	Typical Value	Unit
Die Dimensions (L x W)	_	mm
Exposed Source Pad Metal Dimensions (LxW) Each		mm
Sense Pad Metal Dimensions (LxW)	Please contact your sales representative to get the	mm
Gate Pad Dimensions (L x W)	detailed information about die layout and dimensions.	mm
Top Side Source metallization (AI)		μm
Top Side Gate metallization (Al)		μm
Bottom Drain metallization (Ni/Ag)		μm

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